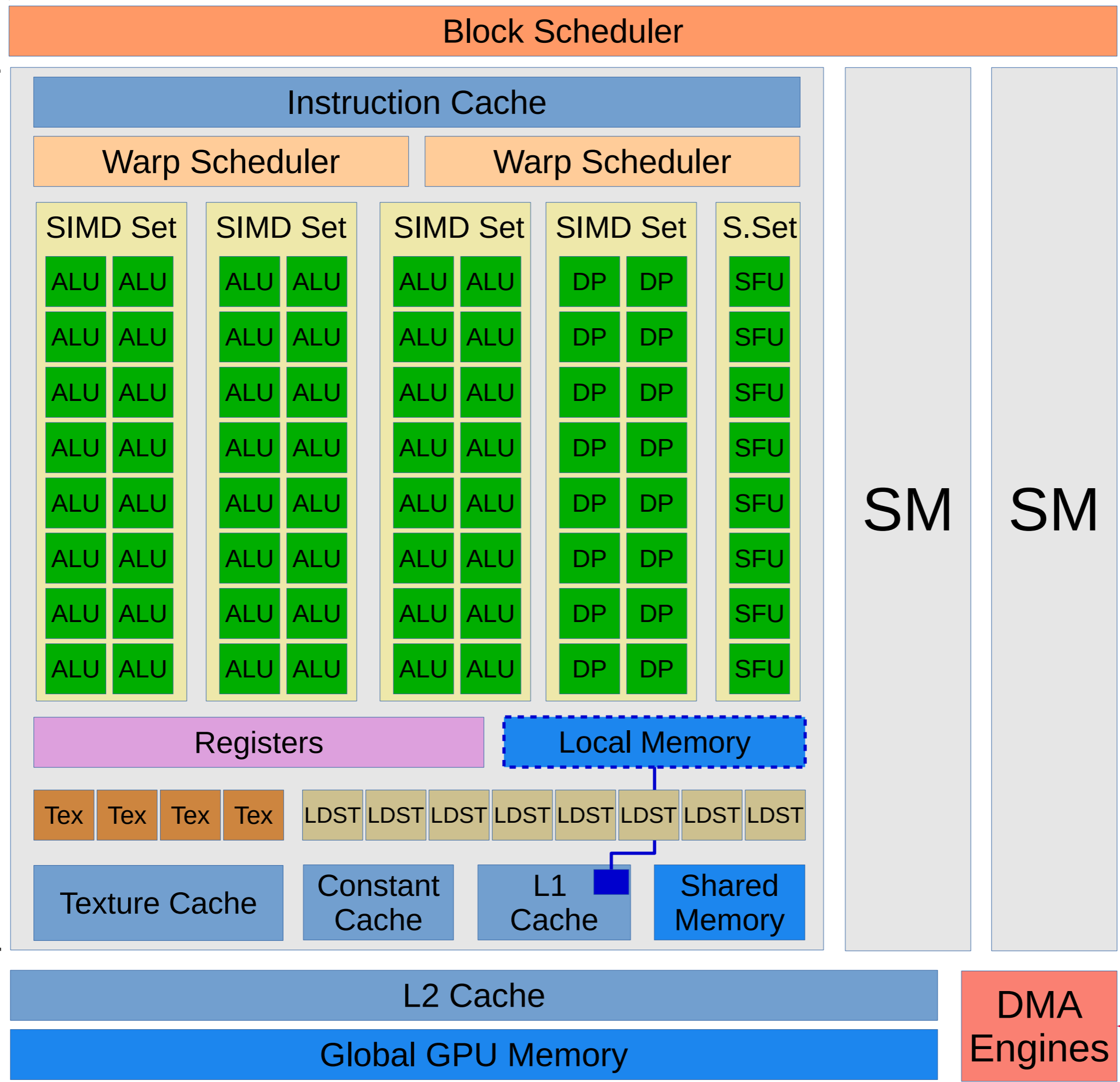


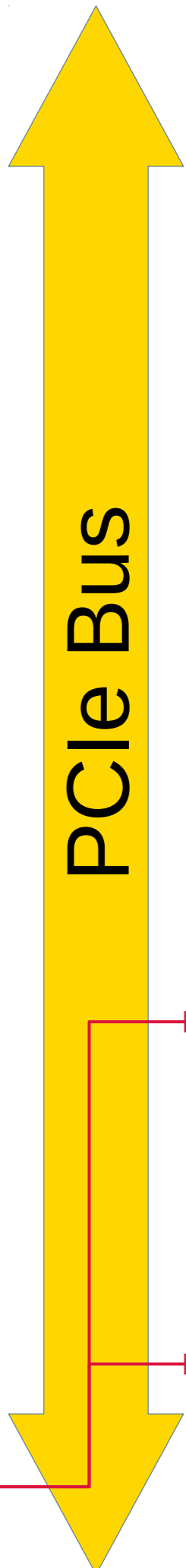
Architecture of Streaming Multiprocessor (SM)

GPU1



SM

SM



GPU2

GPU3

GPU4

Network

FPGA

System Memory